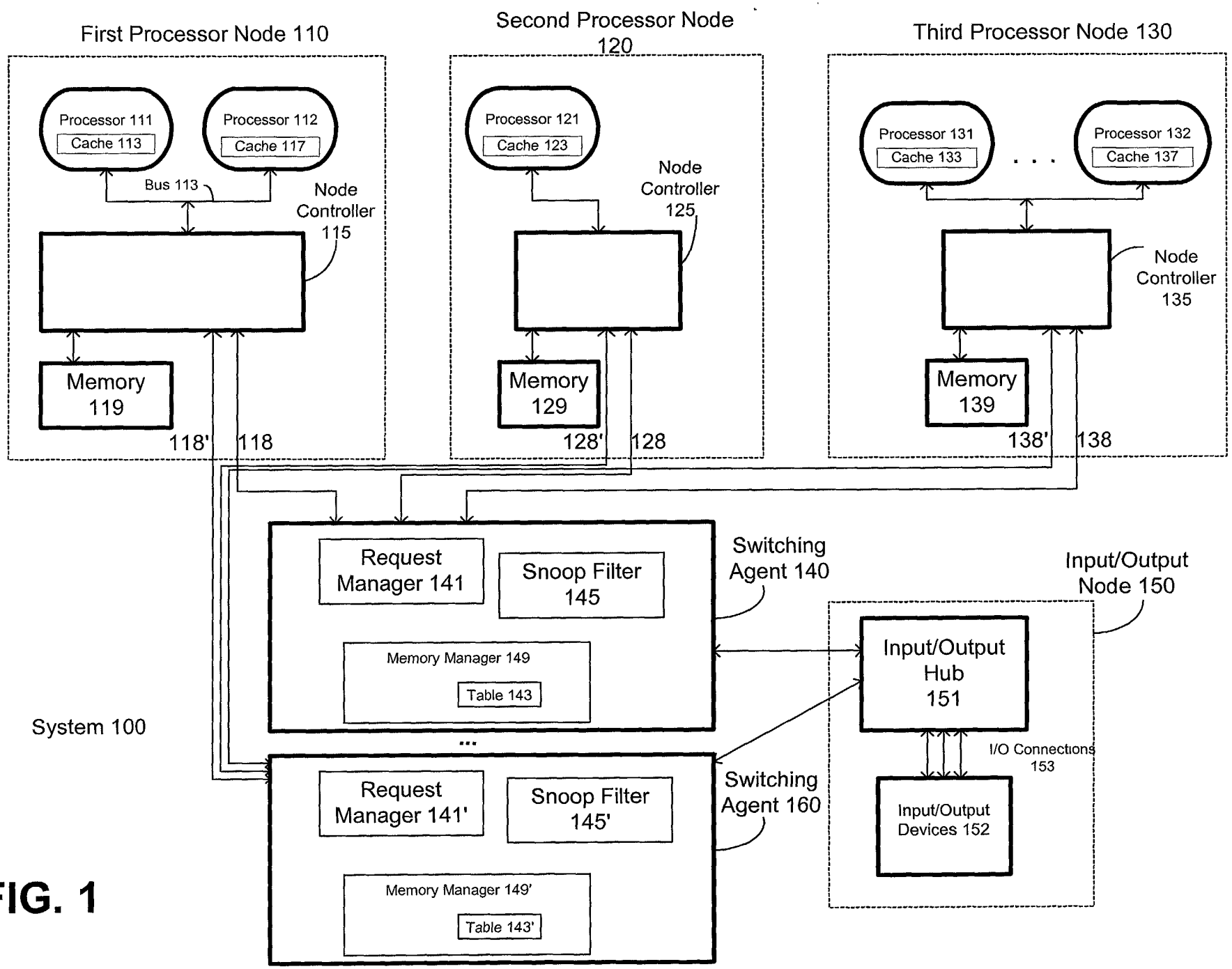


FIG. 1

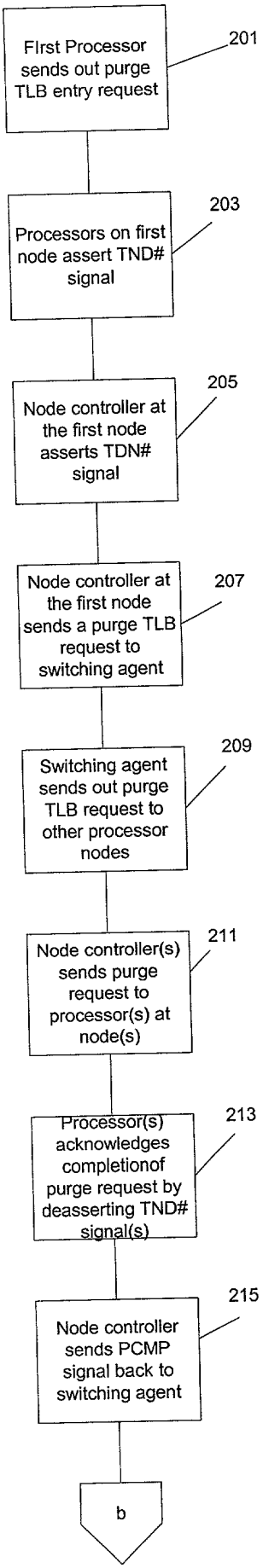
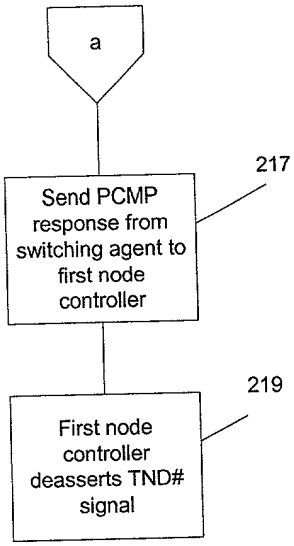

First Processor sends out purge TLB entry request
201
Processors on first node assert TND# signal
203
Node controller at the first node asserts TDN# signal
205
Node controller at the first node sends a purge TLB request to switching agent
207
Switching agent sends out purge TLB request to other processor nodes
209
Node controller(s) sends purge request to processor(s) at node(s)
211
Processor(s) acknowledges completionof purge request by deasserting TND# signal(s)
213
Node controller sends PCMP signal back to switching agent
215
b

Fig 2a


a
217
Send PCMP response from switching agent to first node controller
219
First node controller deasserts TND# signal